

In re PATENT APPLICATION of

John MACNEIL

Group Art Unit: 1762

Serial No.: 10/638,424

Examiner: Unknown

Filed: August 12, 2003

Attorney Docket No. WLJ.068D

For:

DIELECTRIC LAYER FOR A SEMICONDUCTOR DEVICE AND METHOD OF

PRODUCING THE SAME

REQUEST FOR CORRECTED FILING RECEIPT

U.S. Patent and Trademark Office 2011 South Clark Place Customer Window, Mail Stop Application Number Crystal Plaza Two, Lobby, Room 1B03 Arlington, VA 22202

Sir:

Attached is a copy of the official filing receipt from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.

Please correct the following data as follows:

Please change the Domestic Priority data as claimed by applicant to:

This application is a DIV of 09/942,933 08/31/2001

which is a CONTINUATION-IN-PART of 09/914,204 08/23/2001

Respectfully submitted,

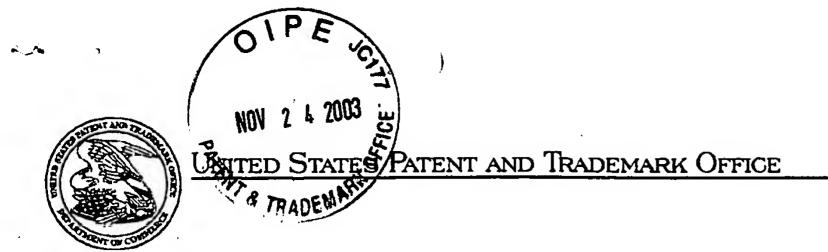
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Dated: November 24, 2003



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/638,424	08/12/2003	1762	750	WLJ.068D	17	4	2

CONFIRMATION NO. 5089

JONES VOLENTINE, P.L.L.C. Suite 150 12200 Sunrise Valley Drive Reston, VA 20191 VOLENTINE FRANCOSILING RECEIPT

OC000000011206820*

NOV 0 7 2003

Date Mailed: 11/06/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by

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Applicant(s)

John MacNeil, Heath, UNITED KINGDOM;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/942,933 08/31/2001
which is a Comparation-th-part of 09/914, 204 08/23/2001

Foreign Applications

UNITED KINGDOM 9904427.3 02/26/1999 UNITED KINGDOM 9922801.7 09/28/1999 UNITED KINGDOM 0000780.7 01/14/2000

If Required, Foreign Filing License Granted: 11/06/2003

Projected Publication Date: To Be Determined - pending completion of Corrected Papers

Non-Publication Request: No

Early Publication Request: No

Title

Dielectric layer for a semiconductor device and method of producing the same

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